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U.S. UTILITY Patent Application

 O.I.P.E. SCANNED <u>104</u> O.A. <u>11</u>	PATENT DATE
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APPLICATION NO. 09/964472	CONT/PRIOR	CLASS 438	SUBCLASS 199	ART UNIT 2812	EXAMINER C. A. H.
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APPLICANTS

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Method of making high-voltage bipolar/CMOS/DMOS (BCD) devices

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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